



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of : Group Art Unit:3206
 Khandros et al. :
 Application Serial No. 08/984,615 : Examiner:
 Filed: December 3, 1997 : Date: April 2, 1998
 For: SEMICONDUCTOR CHIP PACKAGE :
 WITH CENTER CONTACTS :
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Assistant Commissioner For Patents
 Washington, D.C. 20231

INFORMATION DISCLOSURE STATEMENT

Sir:

It is respectfully requested that the references listed on the enclosed Form PTO-1449 be made of record and considered with respect to the above-referenced U.S. Patent application. A copy of each reference was of record in Application Serial No. 08/861,280, the benefit of which is claimed under 35 USC §120. Submission of the present Information Disclosure Statement should not be taken as an admission that the cited references are legally available prior art or that the same are pertinent or material.

Respectfully submitted,

LERNER, DAVID, LITTENBERG,

KRUMHOLZ & MENTLIK

Michael J. Doherty
 MICHAEL J. DOHERTY
 Reg. No. 40,592

600 South Avenue West
 Westfield, NJ 07090
 Tel: (908) 654-5000
 Fax: (908) 654-7866

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I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class mail in an envelope addressed to Assistant Commissioner for Patents, Washington, D.C. 20231 on April 2, 1998.

Michael J. Doherty

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